



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jung et al.

Serial No. 09/941,091

Group Art Unit: 2182

Filed: August 28, 2001

INTEGRATED CIRCUIT MEMORY SYSTEMS HAVING PROGRAMMABLE

SIGNAL BUFFERS FOR ADJUSTING SIGNAL TRANSMISSION DELAYS AND

METHODS OF OPERATING SAME

December 18, 2002

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Commissioner for Patents Washington, DC 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,

D. Scott Moore

Registration No. 42,011

Correspondence Address:

PATENT TRADEMARK OFFICE

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on December 18, 2002.

Traci A. Brown

Date of Signature: December 18, 2002

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FORM PTO	Pat	U.S. Department ent and Trademark	Office ;	Attorney Docket Number: 5649-886			Serial No.: 09/941,091d	
(Use several sheets if necessary)					Applicants: Jung et al.			
					Filing Date: 8/28/01			Group: 2182
			U. S	S. PATENT DO	CUMENTS			
Examiner Initial		Document Number	Date	Name		Class	Subclass	Filing Date if Appropriate
	1	5,946,712	8/31/1999	Lu et al.	Lu et al. 711		167	
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		Document Number	Date		untry	Class	Subclass	Translation Yes No
	2	JP10232818	7/29/1998	Japan				X - Abstract
	<u> </u>				<u> </u>			
								
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		OTHER DO	CUMENTS (I	ncluding Author	r, Title, Date, Pe	ertinent Pages	s, Etc.)	
	3	Notice to Subm	bmit Response, Korean Application No. 10-2000-0050164, September 26, 2002.					2002.
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